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Time-interleaved Analog-to-Digital Converters describes the research performed on low-power time-interleaved ADCs. A detailed theoretical analysis is made of the time-interleaved Track & Hold, since it must be capable of handling signals in the GHz range with little distortion, and minimal power consumption. Timing calibration is not

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attractive, therefore design techniques are presented which do not require timing calibration. The design of power efficient sub-ADCs is addressed with a theoretical analysis of a successive approximation converter and a pipeline converter. It turns out that the first can consume about 10 times less power than the latter, and this conclusion is supported by literature. Time-interleaved Analog-to-Digital Converters describes the design of a high performance time-interleaved ADC, with much attention for practical design aspects, aiming at both industry and research. Measurements show best-inclass performance with a sample-rate of 1.8 GS/s, 7.9 ENOBs and a power efficiency of 1 pJ/conversion-step.

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This advanced text and reference covers the design and implementation of integrated circuits for analog-to-digital and digital-to-analog conversion. It begins with basic concepts and systematically leads the reader to advanced topics, describing design issues and techniques at both circuit and system level. Gain a system-level perspective of data conversion units and their trade-offs with this state-of-the art book. Topics covered include: sampling circuits and architectures, D/A and A/D architectures; comparator and op amp design; calibration techniques; testing and characterization; and more!

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Administration, and permitted to do business in the U.S., with addresses and telephone numbers. Organized by FDA medical device name, in alphabetical order. Keyword index to FDA established standard names of medical devices.

The increasing data rate of wireline communication systems leads to more inter-symbol interference, due to the dispersive properties of the communication channel. This requires more complex equalization blocks to meet the required bit-error rate. One solution is to use an Analog-to-Digital Converter (ADC) in the front-end, thus enabling a digitally-equalized serial link. To achieve the high-data rates of these communication systems, a time-interleaved ADC is typically used. However, this type of ADC suffers from several time-varying errors, the most

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prominent of which is timing skew. This thesis introduces a statistics-based background calibration algorithm that compensates for the effect of timing skew. To demonstrate the background calibration algorithm, a proof-of-concept 5 bit 12 GS/s flash ADC has been fabricated in a 65 nm CMOS process. The design of this ADC takes into consideration the tight power bounds imposed on serial links by optimizing both the time-interleaved and the sub-ADC architecture. Power consumption is further reduced by using calibration circuits to correct the offset of the flash ADC's comparators. In the measured results, the timing skew correction improves the dynamic performance of the time-interleaved ADC by 12 dB, and the proof-of-concept ADC has the lowest published power consumption for ADCs with sample rates higher than 10 GS/s.

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Ronney is an introverted young woman with a disgraceful appearance. She lives humbly in one of the poorest neighborhoods of Sheryl Valley, a town corrupted by the mafia in Southern California. With no diploma, she works hard in her parents' restaurant and provides voice-overs for children's animated movies during the weekend. In accordance with a long-standing family tradition, Ronney's twenty-fifth birthday celebration comes with a dare from her cousins: she must knock on the front door of the infamous Khan household. The Khans' reputation precedes them, rumored to be in association with the mafia. But when Ronney knocks on the door, before she has the chance to run, the Khan family matriarch, Camilia, takes an interest in Ronney. Ronney's lack of conventional beauty and disinterest in fashion draws

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Camilia in, leading her to offer Ronney the position of personal assistant to her eldest son, Yeraz, with a substantial salary at stake. It's an offer Ronney cannot refuse. To keep her job, Ronney's task is simple: do not fall in love with Yeraz. "Easy," she thinks. But what if destiny decides otherwise? Ugly Ronney is a romance in which the heroes enter the gallery of legendary lovers.

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